JC857 U.S. PTO
09/964736
09/28/01

U.S. **UTILITY** Patent Application

O.I.P.E.	PATENT DATE
SCANNED *HKM4* O.A. *All 4*	

KC2 85

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/964736		~~356~~ 710	100	~~2841~~ 2112 2184	~~Wong~~ Patel

APPLICANTS

Mark Eidson

TITLE

Method and system for attaching multiple clock sources to an SDRAM memory array

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	**SUBCLASS**	**CLASS**	**SUBCLASS (ONE SUBCLASS PER BLOCK)**					
INTERNATIONAL CLASSIFICATION								
							☐ Continued on Issue Slip Inside File Jacket	

TERMINAL DISCLAIMER ☐	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

(Assistant Examiner) (Date)

NOTICE OF ALLOWANCE MAILED

☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____

ISSUE FEE

Amount Due	Date Paid

(Primary Examiner) (Date)

ISSUE BATCH NUMBER

☐ The terminal _____ months of this patent have been disclaimed.

(Legal Instruments Examiner) (Date)

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-436A
(Rev. 6/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM
(Attached in pocket on right inside flap)

(FACE)